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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/700,790	11/03/2003	Ernest Allen III	03-0722/LSI1P231	5485	
7590 07/29/2005		EXAMINER			
LSI Logic Corporation 1551 McCarthy Boulevard			NGHIEM, MICHAEL P		
Milpitas, CA			ART UNIT	PAPER NUMBER	
			2863		
			DATE MAILED: 07/29/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)				
Office Action Commence	10/700,790	ALLEN ET AL.	(m)			
Office Action Summary	Examiner	Art Unit				
	Michael P. Nghiem	2863				
The MAILING DATE of this communication apperiod for Reply	pears on the cover sheet with the c	correspondence ad	ldress			
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	I36(a). In no event, however, may a reply be ting the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timel the mailing date of this c D (35 U.S.C. § 133).				
Status	•					
1) Responsive to communication(s) filed on 16 M	May 2005.	•				
2a)⊠ This action is FINAL . 2b)☐ This	s action is non-final.					
3) Since this application is in condition for allowated closed in accordance with the practice under the condition of the	• 0 / 1		e merits is			
Disposition of Claims						
4) ☐ Claim(s) 1-21 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) 1-17 is/are allowed. 6) ☐ Claim(s) 18 is/are rejected. 7) ☐ Claim(s) 19-21 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/o	wn from consideration.					
Application Papers						
9) The specification is objected to by the Examine	er.					
10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.						
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	* * * * * * * * * * * * * * * * * * * *	•				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea	ts have been received. ts have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National	Stage			
* See the attached detailed Office action for a list	of the certified copies not receive	ed.	•			
Attachment(s)						
1) X Notice of References Cited (PTO-892)	4) 🔲 Interview Summary					
 Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	atent Application (PTC	O-152)			
						

DETAILED ACTION

The Amendment filed on May 16, 2005 has been acknowledged.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 18 is rejected under 35 U.S.C. 102(b) as being anticipated by Wilhelm et al. (US 6,211,623).

Regarding claim 18, Wilhelm et al. discloses an integrated circuit (Fig. 3) adapted for voltage level detection (Fig. 3), the integrated circuit comprising:

- a plurality of voltage supply conductors (conductors from CT) configured for providing a power supply voltage (voltage from CT) to the integrated circuit (Fig. 3);
- a monitor cell (comprises comparators, Fig. 3) integrated in the integrated circuit for testing over and under voltage conditions (Fig. 3), the monitor cell comprising a first digital voltage comparator (comparator before 74, Fig. 3) and a second digital voltage comparator (comparator before 72, Fig. 3) each of the comparators coupled

respectively to an associated latch (74, 72) for receiving the output of the voltage comparator (Fig. 3), wherein:

- inputs to the first comparator comprise a supply voltage (voltage from CT) from

one of the plurality of supply conductors and an overvoltage reference voltage (4.0 V);

- inputs to the second comparator comprise the supply voltage (CT) from the one

of the plurality of supply conductors and an undervoltage reference voltage (2.0 V).

Allowable Subject Matter

Claims 19-21 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 1-17 are allowed.

Reasons For Allowance

The combination as claimed wherein applying a second reference voltage having a value different from the first reference voltage to the first input of the first monitor cell (claim 1) or storing the first and second determinations in a first latch and a second latch attached respectively to the first and second comparators (claim 14) or 1the monitor cell is further configured to receive at an input of the first comparator and at an input of the

second comparator a reference voltage provided by one of the plurality of voltage supply conductors for comparison with the supply voltage (claim 19) or the monitor cell is configured to receive a reference voltage from automated test equipment and to transmit outputs of the associated latches to the automated test equipment (claim 20) or the latches of monitor cell each include a timer configured to timestamp occurrences of overvoltage incidents and undervoltage incidents (claim 21) is not disclosed, suggested, or made obvious by the prior art of record.

Response to Arguments

Applicant's arguments filed May 16, 2005 have been fully considered but they are not persuasive in view of the ground of rejection above.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

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mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael P Nghiem whose telephone number is (571) 272-2277. The examiner can normally be reached on M-H.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on (571) 272-2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8000.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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MICHAEL NGHIEM
PRIMARY EXAMINER
Michael Nghiem

July 27, 2005